

**VCXO-Sxxx-LF****1. Specification**

Nominal frequency ( $T_A = +25\text{ }^\circ\text{C}$ ; $V_C = +1.65\text{ V}$ ):	184.32MHz
Nominal frequency tolerance after reflow soldering:	$\leq \pm 10\text{ ppm}$
Frequency stability: in the temperature range $-40\text{ }^\circ\text{C}$ to $+85\text{ }^\circ\text{C}$ : vs. supply voltage changes $UB \pm 5\%$ : vs. load changes $\pm 10\%$ :	$\leq +20\text{ ppm}$ $\leq +3\text{ ppm}$ $\leq \pm 1\text{ ppm}$
Aging:	$\leq \pm 1\text{ ppm per year}$
Frequency control range:	$\geq \pm 80\text{ ppm}$ , $\leq \pm 140\text{ ppm}$
Control voltage $V_C$ : Control voltage input impedance:	$+0.3\text{ V to }+3.0\text{ V}$ $\geq 1\text{ MOhm}$
Frequency modulation:	3 dB cut-off frequency $> 5\text{ kHz}$
Transfer function / Linearity:	positive / 10 %
Supply voltage $V_S$ :	$3.3\text{ V} \pm 5\%$
Current consumption:	$\leq 90\text{ mA}$
Output voltage: load: duty cycle: rise time, fall time:	LVPECL complementary output 50 Ohm 45 / 55 % < 0,5 ns
Non and subharmonics suppression, Spurious:	min 100 dB
Phase noise: 10 Hz: 100 Hz: 1 kHz: 10 kHz: 100 kHz:	< -65 dBc / Hz < -95 dBc / Hz < -125 dBc / Hz < -140 dBc / Hz < -145 dBc / Hz
TriState function at Pin 2:	Logic low or not connected : Enable Logic high : Disable
Temperature ranges Operating: Storage:	$-40\text{ }^\circ\text{C} \dots +85\text{ }^\circ\text{C}$ $-55\text{ }^\circ\text{C} \dots +90\text{ }^\circ\text{C}$

**2. Environmental conditions**

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3				
2				
1		22.11.10	J. Müller	
ED	Description	Date	Name	



# VCXO-Sxxx-LF



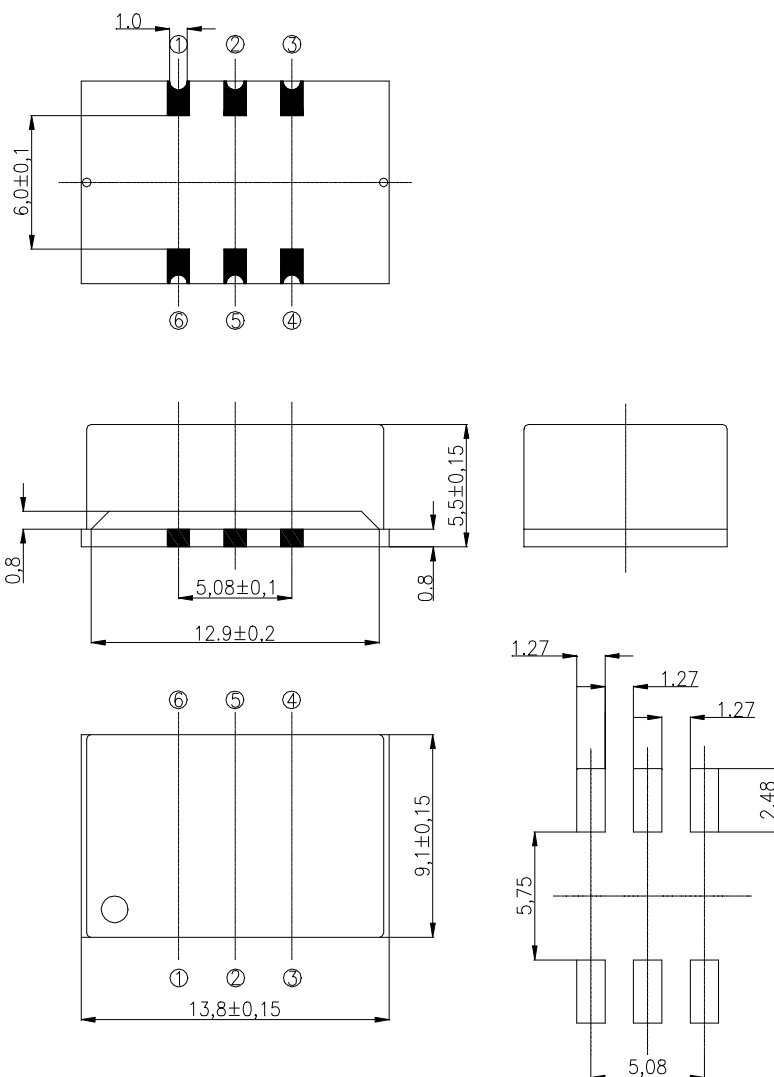
According to KVG Product Qualification Procedure AA-QM-200

### 3. Marking

Manufacturer's name, date code(week/year)  
 Specification  
 Center frequency

### 4. Case

Case style: **BF-157-5.5D**



### Pin configuration

1. Control voltage  $V_C$
2. TriState, Enable/Disable
3. GND
4. RF-output
5. Complementary RF output
6. Supply voltage  $V_S$

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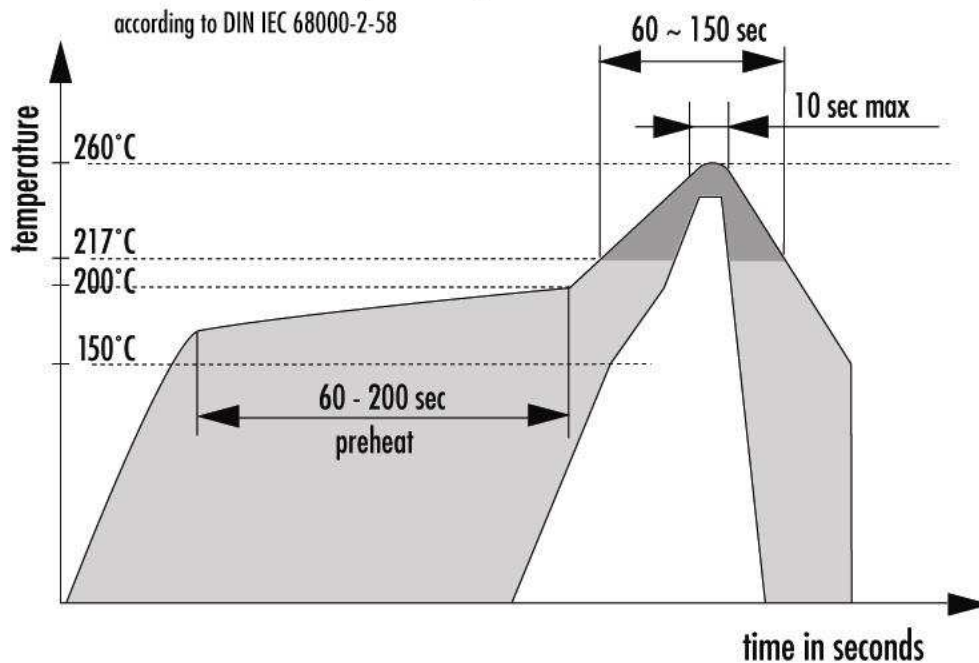
## 5. ROHS Info

Moisture Sensitivity Level: 2  
 Termination finish: AuNi  
 Max. reflow temperature: 260°C

## 6. Soldering profile

### Lead Free reflow temperature profile

according to DIN IEC 68000-2-58



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